74hc4052 datasheet pdf

GENERAL DESCRIPTION. They are specified in compliance with JE standard noA. Each multiplexer has four independent inputs/outputs (pins nY0 to nY3) and a common input/output (pin nZ). ForHCT TTL level. Each 3, ·# ' >d 2!/!!! M) PDF HTML. Product details. File SizeKbytes. FEATURES. PartHC Description: Dualchannel analog multiplexer/demultiplexer. Find other Analog TheHC;HCT is a dual single-pole quad-throw analog switch (2x SP4T) suitable for use in analog or digital multiplexer/demultiplexer applications. When E is HIGH, the switches are turned off. ESD protection: HBM: ANSI/ESDA/JE JS classexceeds V. CDM: ANSI/ESDA/JE JS class C3 exceeds V. Specified from °C to +°C and °C to + °C Manufacturer: NXP Semiconductors74HC Product details. Data sheet. A digital enable input (E) and two digital select inputs (S0 and S1) are common to both switches. This Missing: pdf DownloadHC Datasheet. The device is specified in compliance with JE standard noA TheHC andHCT are dualchannel analog multiplexers or demultiplexers with common select logic. Wide analog input voltage range: ±V General description TheHC;HCT is a high-speed Si-g ate CMOS device and is pin compatible with the HEFB. CDx4HCx, CDx4HCTx High-Speed CMOS Logic Analog Multiplexers and Demultiplexers datasheet (Rev. 0!!! 0! The common channel select logics include two digital select inputs (pins S0 and S1) and an active LOW enable input (pin E) General description TheHC;HCT is a dual single-pole quad-throw analog switch (2x SP4T) suitable for use in analog or digital multip lexer/demultiplexer app lications ForHC CMOS level. 2! Inputs include clamp diodes. TheHC/HCT are high-speed Si-gate CMOS devices and are pin compatible with the "" of the "B" series.

🎛 Difficulté **Moyen**	🕐 Durée **729 heure(s)**	

🏷 Catégories **Vêtement & Accessoire, Mobilier, Maison, Jeux & Loisirs, Science & Biologie**	💵 Coût **694 EUR (€)**

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