74 series ic pin diagram pdf

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Once it's connected, you can use any of the four basic OR gates inside TheHCprovides provides four independentinput NAND gates with standard push-pull outputs. For simplicity the family letters after theare omitted in the diagrams below because the pin connections apply to all ICs with the same number. One difference between the HC and LS version of the chip is that the HC supports 2V to 6V, while the LS supports only 5V. The SN74HC is anbit shift register with serial inputs (A and B) connected through an AND gate, as well as an asynchronous clear (CLR). The device is designed for operation with a power supply range of V to V. The gates perform the Boolean function: Y = A B or Y = A + B VCC = PinGND = PinConnection Diagram Pin Descriptions NoteOC—Open Collector Order Number Package Number Package Description DM74LS47M M16A Lead Small Outline Integrated Circuit (SOIC), JE MS, Narrow DM74LS47N N16E Lead Plastic Dual-In-Line Package (PDIP), JE MS, Wide Pin Names Description A0-A3 BCD The DM74LSaccepts four lines of BCD () input n Open-collector outputs data, generates their complements internally and odes n Drive indicator segments directly TI's SN74LSA is a Quad /S-/R latches. The device requires a high signal on both A and B in order to set the input data line high; a low signal on either input will set the input data line low Like every other chip in the series, this IC needs to be connected to power before you can use it. TheHC comes in a pin package, and you need to connect it to power before you can use it. One For each IC there is a diagram showing the pin arrangement and brief notes explain the function of the pins where necessary. For example NAND gates are available asHC00,HCTandLS00 Overview. Most ICs support a VCC voltage of 5V. Most ICs support a VCC voltage of 5V. Find parameters, ordering and quality information IC Test Clip Series • AWG insulated gold contacts Color: white Part No. Product No. Description JTCpin (for 8, and pin ICs) \$ The HCis identical in pinout to the LS The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs How To Use This Chip.



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